

Maharashtra Board Class 12 Electronics Question Paper 2026

Time Allowed :3 Hours	Maximum Marks :50	Total Questions :5
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General Instructions

1. All questions are compulsory
2. Figures to the right indicate full marks.
3. Draw neat labelled diagrams wherever necessary.
4. Use of logtable is allowed.

1(A)(a). In astable multivibrator if charging time constant is equal to discharging time constant, duty cycle is _____.

- (i) 50%
 - (ii) 100%
 - (iii) 1%
 - (iv) 25%
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1(A)(b). The area to be served by a cellular telephone system is divided into _____.

- (i) region
 - (ii) cells
 - (iii) MTSO
 - (iv) rings
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1(A)(c). _____ stage of operational amplifier rejects the noise.

- (i) Level Shifter
 - (ii) Differential Amplifier
 - (iii) Emitter Follower
 - (iv) Output
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1(A)(d). The semiconductor diode is used as _____.

- (i) Temperature Transducer
 - (ii) Pressure Transducer
 - (iii) Displacement Transducer
 - (iv) Piezoelectric Transducer
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1(B)(a). A Zener voltage regulator is to be designed for output of 10 volts. If the input voltage to the regulator is 25 volts. Find the value of current limiting resistor. Given $P_z = 500 \text{ mW}$.

1(B)(b). What are the drawbacks of RC coupled amplifier ?

1(B)(c). Explain the use of Cathode Ray Oscilloscope for measurement of AC and DC voltage.

2(A)(a). Draw the diagram showing pin connection of IC-555 and IC LM317.

2(A)(b). What do you mean by Network Topology ? Enlist the types of network topologies and explain any one of them with diagram.

2(A)(c). Write a note on LDR.

2(B)(a). In a circuit of Schmitt trigger $R_1 = 10 \text{ k}\Omega$, $R_2 = 2 \text{ k}\Omega$, $V_{in} = 3 \text{ V}_{p-p}$ sinewave, with saturation voltage of ± 13 volts. Calculate: (i) UTP, (ii) LTP, (iii) Hysteresis voltage, (iv) Feedback factor β .

2(B)(b). State eight advantages of fibre optic cable over conventional electrical cable for communication.

3(A)(a). The turns ratio of transformer in a bridge rectifier is 12:1. Primary is connected to 230 volts, 50 Hz AC mains. Find the output of DC voltage under no load condition, assume the voltage drop across each diode to be zero.

3(A)(b). The deflection sensitivity of CRT is 0.04 mm/v. An unknown voltage applied to X-plates causes the deflection of spot 5mm horizontally. Find the value of unknown voltage.

3(A)(c). Explain the working of Piezoelectric Transducer.

3(B)(a). List any four advantages of SMPS.

3(B)(b). Draw the circuit diagram of Inverting Operational Amplifier and obtain an expression for its gain. Explain how it can be used as sign changer.

4(A)(a). Explain the working of IC 555 as a Monostable Multivibrator.

4(A)(b). List three applications of simplex and three applications of duplex communication system.

4(A)(c). Explain how CRO displays a waveform.

4(B)(a). State any eight characteristics of ideal Op-Amplifier.

4(B)(b). Draw internal block diagram of IC555 and explain its working.

5(A)(a). Define deflection sensitivity of CRO. What is the function of delay line in CRO ?

5(A)(b). A single phase half wave rectifier supplies power to $1\text{ k}\Omega$ load. The input supply voltage is 220 V_{RMS} . Neglect the forward resistance of diode. Calculate average voltage and current in the circuit.

5(A)(c). What is cell in cellular phone system ? Explain in brief.

5(B)(a). Explain the working of centre tapped full wave rectifier and compare any two parameters in case of centre tapped full wave rectifier and bridge rectifier.

5(B)(b). State any two Linear and Non-linear applications of Op-Amp.

OR.

5(A)(a). State any three applications of function generator.

5(A)(b). Explain the working of Loudspeaker with neat diagram.

5(A)(c). State any three advantages of Op-Amp over normal amplifier.

5(B)(a). State any four points of comparison between Amplitude Modulation and Frequency Modulation.

5(B)(b). Give one advantage and one disadvantages of following types of filter circuits: (i) Capacitor input filter (ii) Inductor filter (iii) R.C. filter (iv) L.C. filter
